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|  | **ECTS Course Description Form** |
| **PART I ( Senate Approval)** |
| **Offering School**  | **Engineering** |
| **Offering Department** | **Electrical and Electronics Engineering** |
| **Program(s) Offered to** | **Electrical and Electronics Engineering** | **Compulsory** |
| **Computer Engineering** | **Elective** |
| **Industrial Engineering** | **Elective** |
| **Course Code**  | **EE 401** |
| **Course Name** | **Digital Electronics** |
| **Language of Instruction** | **English** |
| **Type of Course** | *Course* |
| **Level of Course** | **Undergraduate** |
| **Hours per Week** | **Lecture: 3** | **Laboratory:**  | **Recitation:**  | **Practical:**  | **Studio:** | **Other:** |
| **ECTS Credit** | **6** |
| **Grading Mode** | **Letter Grade** |
| **Pre-requisites** | *EE 202* |
| **Co-requisites** | **-** |
| **Registration Restriction** | *-* |
| **Educational Objective** |

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|  This course aims to equip the students with the fundamentals of digital electronic circuit design. Diode resistor logic, resistor transistor logic, transistor transistor logic, bjt dc analysis, MOSFET dc analysis, CMOS inverter, pseudo CMOS logic, regenerative circuits, Schmitt trigger, latches, flip flops, multivibrator circuits, 555 timer circuit, RAM, ROM, ADCs, DACs are covered.  |

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| **Course description** |

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|  Diode resistor logic, resistor transistor logic, transistor transistor logic, bjt dc analysis, MOSFET dc analysis, CMOS inverter, pseudo CMOS logic, regenerative circuits, Schmitt trigger, latches, flip flops, multivibrator circuits, 555 timer circuit, RAM, ROM, ADCs, DACs |

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| **Learning Outcomes** | **LO1** | Perform dc analysis on transistor circuits  |
| **LO2** | Design logic circuits using diode resistor, resistor transistor, transistor transistor logics |
| **LO3** | Analyze CMOS logic gates |
| **LO4** | Design and analyse Schmitt trigger circuit |
| **LO5** | Analyze multivibrator and 555 timer circuits |
| **LO6** | Design RAM, ROM circuits |
| **LO7** | Analyze ADCs and DACs |
| **PART II ( Faculty Board Approval)** |
| **Basic Outcomes (University-wide)** | **No.** | **Program Outcomes** | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** | **LO7** |
| **PO1** | **Ability** to communicate effectively and write and present a report in Turkish and English.  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO2** | **Ability** to work individually, and in intra-disciplinary and multi-disciplinary teams. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO3** | **Recognition** of the need for life-long learning and **ability** to access information , follow developments in science and technology, and continually reinvent oneself. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO4** | **Knowledge** of project management, risk management, innovation and change management, entrepreneurship, and sustainable development. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO5** | **Awareness** of sectors and **ability** to prepare a business plan. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO6** | **Understanding** of professional and ethical responsibility and **demonstrating** ethical behavior. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Faculty Specific Outcomes** | **PO7** | **Ability to develop, select and use modern techniques and tools necessary for engineering applications and ability to use information technologies effectively.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO8** | **Recognition of the effects of engineering applications on health, environment and safety in the universal and societal dimensions and the problems of the time and awareness of the legal consequences of engineering solutions.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO9** | **Ability to identify, define, formulate and solve complex engineering problems; and electing and applying appropriate analysis and modelling methods for this purpose.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Discipline Specific Outcomes (program)** | **PO10** | **Gains comprehensive knowledge in mathematics, natural sciences, related engineering fields and general engineering subjects.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO11** | **Able to identify complex engineering problems and solve them with appropriate methods of analysis.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO12** | **Able to design a complex electronic system that meets the desired performance by using modern design techniques and taking real life conditions into account.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO13** | **Able to develop new techniques and tools for solution of current engineering problems.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO14** | **Able to use computer software and hardware technologies together with information technologies in an effective way.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO15** | **Able to produce innovative solutions for solution of current engineering problems by gathering data through experiment design and interpretation of results.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO16** | **Able to actively work individually or in teams where engineers from the same or different disciplines are involved.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO17** | **Gains competency in effective written and verbal communication, presentation and preparation of technical reports in Turkish and English.**  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO18** | **Constantly increases knowledge with the awareness of lifelong learning by closely following the developments in science and technology .** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO19** | **Acts in accordance with scientific and ethical principles and the standards used in engineering practice at every stage of career** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO20** | **Able to describe concepts related to business life such as project management, risk management, change management, entrepreneurship and sustainability.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO21** | **Gains awareness of the legal consequences of engineering solutions developed together with the effect of engineering applications on health, environment and safety on a universal and social scale.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO22** | **Able to solve problems involving probability and statistics, derivative and integral calculations, multivariable mathematics, linear algebra, differential equations, and complex variables, and their electrical and electronics applications.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO23** | **Able to organize projects and events for the social environment they live in with the awareness of social responsibility and implement them.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO24** | **Able to plan and direct activities for employees under their responsibility to develop within the framework of a project.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
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| **PART III ( Department Board Approval)** |
| **Course Subjects, Contribution of Course Subjects to Learning Outcomes, and Methods for Assessing Learning of Course Subjects** | **Subjects** | **Week** |  | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** |
| **S1** | *1* | Diode resistor logic  | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S2** | *2* | Resistor transistor logic, transistor transistor logic | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S3** | *3* | BJT and MOSFET circuit analysis  | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S4** | 4 | CMOS inverter | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S5** | 5 | Pseudo CMOS logic | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S6** | 6 | Regenerative circuits | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S7** | 7 | Schmitt trigger | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S8** | 8 | Latches | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S9** | 9 | Flip flops | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S10** | 10 | Multivibrator circuits | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S11** | 11 | 555 timer circuit | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S12** | 12 | RAMs and ROMS | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S13** | 13 | ADCs | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **S14** | 14 | DACs | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* | *A1-A4* |
| **Assessment Methods, Weight in Course Grade, Implementation and Make-Up Rules**  | **No.** | **Type** | **Weight** | **Implementation Rule** | **Make-Up Rule** |
| **A1** | **Exam** | *%70* | *No electronics allowed in the exams except calculators*  | The student is informed about a make up exam in case his/her excuse is valid and an accompanying doctors report is provided. |
| **A2** | **Quiz** |  |  |  |
| **A3** | **Homework** |  |  |  |
| **A4** | **Project** | *%30* | *Projects determined as part of the class are performed by the students in groups. Project demonstration is made on a day during finals week. A group report is submitted at the last day of the final exams.* | The student is informed about a make up project demonstration in case his/her excuse is valid and an accompanying doctors report is provided. |
| **A5** | **Report** |  | - | - |
| **A6** | **Presentation** |  | - | - |
| **A7** | **Attendance/ Interaction** |  | - | - |
| **A8** | **Class/Lab./****Field Work** |  | Experiments as part of the class are performed by the students in groups. A group report is submitted until the end of the following week. | The student is informed about a make up lab in case his/her excuse is valid and an accompanying doctors report is provided. |
| **A9** | **Other** |  |  |  |
| **TOTAL** | **100%** |
| **Evidence of Achievement of Learning Outcomes** | At least one question from each subject is asked during the exams. Students are required to perform a project that involve the class content, and write a report for the project. A weighted average is calculated for each student based on the percentage of each assessment method. Students are required to collect a minimum score over 100, which is announced by the instructor, to pass the course. This score is determined based on class average. |
| **Method for Determining Letter Grade** | The scores of 3 exams, 3 labs and the project are used to calculate the final score. Maximum scores from each contributor is shown below.

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Assesment | Exam 1 | Exam 2 | Final  | Project | Total |
| Point | 20 | 20 | 30 | 30 | 100 |

The table below is used to conver the total point over 100 to letter grade:

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Point | 100-75 | 74-70  | 69-65 | 64-60 | 59-55 | 54-50 | 49-45 | 44-40 | 39-35 | 34-30 |
| Grade | A | A- | B+ | B | B- | C+ | C | C- | D+ | D |

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| **Teaching Methods, Student Work Load** | **No** | **Method** | **Explanation** | **Hours** |
| ***Time applied by instructor*** |
| **1** | **Lecture** | Class content is explained by writings on the board and computer presentations | *3x14* |
| **2** | **Interactive Lecture** |  |  |
| **3** | **Recitation** | Example questions are solved on the board | *3x8* |
| **4** | **Laboratory** |  |  |
| **5** | **Practical** |  |  |
| **6** | **Field Work** |  |  |
| ***Time expected to be allocated by student*** |
| **7** | **Project** | A project is implemented, and a report is prepared | *60* |
| **8** | **Homework** |  |  |
| **9** | **Pre-class Learning of Course Material**  | Next class’ material is read before the class | *1x14* |
| **10** | **Review of Course Material** | Previous class material is reviewed each week | *1x14* |
| **11** | **Studio** |  |  |
| **12** | **Office Hour** | One-to-one meetings for discussions | *2x14* |
| **TOTAL** | *182* |
| **IV. PART** |
| **Instructor** | **Name** | Mustafa İlker Beyaz |
| **E-mail** | mibeyaz@antalya.edu.tr |
| **Phone Number** | 0242 245 0367 |
| **Office Number** | 0242 245 0367 |
| **Office Hours** | *Determined during each semester, 2 hours per week* |
| **Course Materials** | **Mandatory** |  |
| **Recommended** | *Microelectronics Circuit Analysis and Design, Donald A. Neamen, 4th edition* |
| **Other** | **Scholastic Honesty** | Violations of scholastic honesty include, but are not limited to cheating, plagiarizing, fabricating information or citations, facilitating acts of dishonesty by others, having unauthorized possession of examinations, submitting work of another person or work previously used without informing the instructor, or tampering with the academic work of other students. Any for of scholastic dishonesty is a serious academic violation and will result in a disciplinary action. |
| **Students with disabilities** | Reasonable accommodations will be made for students with verifiable disabilities. |
| **Safety Issues**  | The course does not require any special safety precautions. |
| **Flexibility** | Circumstances may arise during the course that prevents the instructor from fulfilling each and every component of this syllabus; therefore, the syllabus is subject to change.  Students will be notified prior to any changes.  |