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|  | **ECTS Course Description Form** |
| **PART I ( Senate Approval)** |
| **Offering School**  | **Engineering** |
| **Offering Department** | **Electrical and Electronics Engineering** |
| **Program(s) Offered to** | **Electrical and Electronics Engineering** | **Compulsory** |
| **Computer Engineering** | **Compulsory** |
| **Industrial Engineering** |  |
| **Course Code**  | EE 221 |
| **Course Name** | Digital Systems |
| **Language of Instruction** | English |
| **Type of Course** | Lecture  |
| **Level of Course** | Undergrad |
| **Hours per Week** | **Lecture: 3** | **Laboratory:** | **Recitation:**  | **Practical:**  | **Studio:** | **Other:** |
| **ECTS Credit** | **4** |
| **Grading Mode** | Letter grade |
| **Pre-requisites** | - |
| **Co-requisites** | *EE 221 Digital Systems and EE 221L Digital Systems I Laboratory courses should be taken concurrently within a semester* |
| **Registration Restriction** | - |
| **Educational Objective** | The course teaches the students how digital circuits are designed and analyzed. They will be able to apply their gained knowledge to both the analysis and design of combinational and sequential digital circuits. The students also learn how to use hardware description languages to construct such designs. |
| **Course description** | This course introduces the basic concepts of digital systems, such as Number systems. Boolean algebra, logic networks and their simplification, canonical forms. Combinatorial circuits. Adders, decoders, encoders, multiplexers, flip-flops, sequential circuit analysis and design, registers, counters, memory and programmable logic. |
| **Learning Outcomes** | **LO1** | Be able to describe and use a variety of digital circuit components |
| **LO2** | Be able to design and model digital systems, combinational and sequential |
| **LO3** | Be able to use a hardware description language for system modeling and simulation |
| **LO4** | Implement digital systems on reconfigurable programmable logic devices |
| **LO5** | Be able to design and implement hardware digital systems incorporating memory modules. |
| **PART II ( Faculty Board Approval)** |
| **Basic Outcomes (University-wide)** | **No.** | **Program Outcomes** | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** |
| **PO1** | **Ability** to communicate effectively and write and present a report in Turkish and English.  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO2** | **Ability** to work individually, and in intra-disciplinary and multi-disciplinary teams. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO3** | **Recognition** of the need for life-long learning and **ability** to access information , follow developments in science and technology, and continually reinvent oneself. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO4** | **Knowledge** of project management, risk management, innovation and change management, entrepreneurship, and sustainable development. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO5** | **Awareness** of sectors and **ability** to prepare a business plan. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO6** | **Understanding** of professional and ethical responsibility and **demonstrating** ethical behavior. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Faculty Specific Outcomes** | **PO7** | **Ability to develop, select and use modern techniques and tools necessary for engineering applications and ability to use information technologies effectively.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO8** | **Recognition of the effects of engineering applications on health, environment and safety in the universal and societal dimensions and the problems of the time and awareness of the legal consequences of engineering solutions.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO9** | **Ability to identify, define, formulate and solve complex engineering problems; and electing and applying appropriate analysis and modelling methods for this purpose.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Discipline Specific Outcomes (program)** | **PO10** | **Gains comprehensive knowledge in mathematics, natural sciences, related engineering fields and general engineering subjects.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO11** | **Able to identify complex engineering problems and solve them with appropriate methods of analysis.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO12** | **Able to design a complex electronic system that meets the desired performance by using modern design techniques and taking real life conditions into account.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO13** | **Able to develop new techniques and tools for solution of current engineering problems.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO14** | **Able to use computer software and hardware technologies together with information technologies in an effective way.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO15** | **Able to produce innovative solutions for solution of current engineering problems by gathering data through experiment design and interpretation of results.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO16** | **Able to actively work individually or in teams where engineers from the same or different disciplines are involved.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO17** | **Gains competency in effective written and verbal communication, presentation and preparation of technical reports in Turkish and English.**  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO18** | **Constantly increases knowledge with the awareness of lifelong learning by closely following the developments in science and technology .** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO19** | **Acts in accordance with scientific and ethical principles and the standards used in engineering practice at every stage of career** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO20** | **Able to describe concepts related to business life such as project management, risk management, change management, entrepreneurship and sustainability.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO21** | **Gains awareness of the legal consequences of engineering solutions developed together with the effect of engineering applications on health, environment and safety on a universal and social scale.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO22** | **Able to solve problems involving probability and statistics, derivative and integral calculations, multivariable mathematics, linear algebra, differential equations, and complex variables, and their electrical and electronics applications.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO23** | **Able to organize projects and events for the social environment they live in with the awareness of social responsibility and implement them.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO24** | **Able to plan and direct activities for employees under their responsibility to develop within the framework of a project.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
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| **PART III ( Department Board Approval)** |
| **Course Subjects, Contribution of Course Subjects to Learning Outcomes, and Methods for Assessing Learning of Course Subjects** | **Subjects** | **Week** |  | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** |
| **S1** | 1 | Introduction, Number Systems, Binary Numbers | A1-A2 |  |  |  |  |  |
| **S2** | 2 | Logic Gates and Boolean Algebra | A1-A2 | A1-A2 |  |  |  |  |
| **S3** | 3 | Gate-level minimization | A1-A2 | A1-A2 |  |  |  |  |
| **S4** | 4 | Combinational logic design | A1-A2 | A1-A2 | A1-A2 |  |  |  |
| **S5** | 5 | Arithmetic functions | A1-A2 | A1-A2 | A1-A2 |  |  |  |
| **S6** | 6 | Hardware description languages  | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |  |
| **S7** | 7,8 | Sequential Circuit Analysis & Design | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
| **S8** | 9 | Programmable Implementation Technologies (ROM, PLA, PAL) | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
| **S9** | 10 | Registers, Micro-operations and Implementations | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
| **S10** | 11 | Counters, register cells, buses, & serial operations | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
| **S11** | 12 | Memory basics | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
| **S12** | 13,14 | Computer design basics | A1-A2 | A1-A2 | A1-A2 | A1-A2 | A1-A2 |  |
|  |  |  |  |  |  |  |  |  |
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| **Assessment Methods, Weight in Course Grade, Implementation and Make-Up Rules**  | **No.** | **Type** | **Weight** | **Implementation Rule** | **Make-Up Rule** |
| **A1** | **Exam** | 80% | Midterm is 35% and final is 45% of the final grade. | A make-up exam is provided in case of a legitimate reason with a proof. |
| **A2** | **Quiz** | 20% | At least two quizzes are conducted. | No make-up |
| **A3** | **Homework** |  |  |  |
| **A4** | **Project** |  |  | - |
| **A5** | **Report** |  | - | - |
| **A6** | **Presentation** |  | - | - |
| **A7** | **Attendance/ Interaction** |  | - | - |
| **A8** | **Class/Lab./****Field Work** |  |  |  |
| **A9** | **Other** |  |  |  |
| **TOTAL** | **100%** |
| **Evidence of Achievement of Learning Outcomes** | Students will demonstrate learning outcomes through midterm exams, homework assignments, quizzes, and the final exam. Every topic is tested with at least one exam or homework question. In order to pass, a student needs to accumulate at least 50% of the total grade. |
| **Method for Determining Letter Grade** | The overall grade is converted to a letter grade using the table below.

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Total points** | 100-95 | 94-90 | 89-85 | 84-80 | 79-75 | 74-70 | 69-65 | 64-60 | 59-55 | 54-50 |
| **Letter Grade** | A | A- | B+ | B | B- | C+ | C | C- | D+ | D |

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| **Teaching Methods, Student Work Load** | **No** | **Method** | **Explanation** | **Hours** |
| ***Time applied by instructor*** |
| **1** | **Lecture** | Class content is explained by writing on the board and with computer presentations | 4x14 |
| **2** | **Interactive Lecture** |  |  |
| **3** | **Recitation** |  |  |
| **4** | **Laboratory** |  |  |
| **5** | **Practical** |  |  |
| **6** | **Field Work** |  |  |
| ***Time expected to be allocated by student*** |
| **7** | **Project** |  |  |
| **8** | **Homework** | Homework is completed in preparation for quizzes | 2x4 |
| **9** | **Pre-class Learning of Course Material**  | Next class’ material is read before the class | 1x14 |
| **10** | **Review of Course Material** | Previous class material is reviewed each week | 2x14 |
| **11** | **Studio** |  |  |
| **12** | **Office Hour** | One-to-one meetings for discussions | 1x14 |
| **TOTAL** | *120* |
| **IV. PART** |
| **Instructor** | **Name** | Shah Rahman |
| **E-mail** | shah.rahman@antalya.edu.tr |
| **Phone Number** |  |
| **Office Number** | A1-62 |
| **Office Hours** | Determined during each semester, 2 hours per week |
| **Course Materials** | **Mandatory** | *Digital Design (with an introduction to the Verilog HDL),* 5th edition*,* by Mano, M. Morris and Ciletti, Michael. D. |
|
| **Other** | **Scholastic Honesty** | Violations of scholastic honesty include, but are not limited to cheating, plagiarizing, fabricating information or citations, facilitating acts of dishonesty by others, having unauthorized possession of examinations, submitting work of another person or work previously used without informing the instructor, or tampering with the academic work of other students. Any for of scholastic dishonesty is a serious academic violation and will result in a disciplinary action. |
| **Students with disabilities** | Reasonable accommodations will be made for students with verifiable disabilities. |
| **Safety Issues**  | The course does not require any special safety precautions. |
| **Flexibility** | The level of detail can be made more in-depth or can be reduced depending on the students interests and time availability. |