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|  | **ECTS Course Description Form** |
| **PART I ( Senate Approval)** |
| **Offering School**  | **Engineering** |
| **Offering Department** | **Electrical and Electronics Engineering** |
| **Program(s) Offered to** | **Electrical and Electronics Engineering** | **Compulsory** |
| **Computer Engineering** | **Elective** |
| **Industrial Engineering** | **Elective** |
| **Course Code**  | **EE 112** |
| **Course Name** | **Introduction to Electrical and Electronics Engineering** |
| **Language of Instruction** | **English** |
| **Type of Course** | *Course* |
| **Level of Course** | **Undergraduate** |
| **Hours per Week** | **Lecture: 3** | **Laboratory: 0** | **Recitation:** **0** | **Practical: 0** | **Studio:****0** | **Other:****0** |
| **ECTS Credit** | **2** |
| **Grading Mode** | **Letter Grade** |
| **Pre-requisites**  | *-* |
| **Co-requisites** | *-* |
| **Registration Restriction** | *-* |
| **Educational Objective** | To familiarize students in sub-fields of Electrical Engineering (e.g. Signal Processing, Control System, Power System, Telecommunication, Photonics, Microelectronics). |
| **Course description** | An orientation course introducing the students to the engineering in general and electrical and electronics engineering in particular with a discussion of the past, present and future of major areas |
| **Learning Outcomes** | **LO1** | Become familiar with the curricular structure of Electrical and Electronics Engineering |
| **LO2** | Recognise, define and correctly use basic electrical technical terms such as voltage, current, power, phase, impedence, reactance, filtering, etc. |
| **LO3** | Demonstrate familiarity with mathematics, data analysis and programming in Electrical and Electronics Engineering |
| **LO4** | Demonstrate familiarity with wireless communication systems and its standards |
| **LO5** | Demonstrate familiarity with basics and applications of Microwave Engineering Concepts |
| **LO6** | Demonstrate familiarity in device growth process and device fabrication |
| **PART II ( Faculty Board Approval)** |
| **Basic Outcomes (University-wide)** | **No.** | **Program Outcomes** | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** |
| **PO1** | **Ability** to communicate effectively and write and present a report in Turkish and English.  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO2** | **Ability** to work individually, and in intra-disciplinary and multi-disciplinary teams. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO3** | **Recognition** of the need for life-long learning and **ability** to access information , follow developments in science and technology, and continually reinvent oneself. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO4** | **Knowledge** of project management, risk management, innovation and change management, entrepreneurship, and sustainable development. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO5** | **Awareness** of sectors and **ability** to prepare a business plan. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO6** | **Understanding** of professional and ethical responsibility and **demonstrating** ethical behavior. | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Faculty Specific Outcomes** | **PO7** | **Ability to develop, select and use modern techniques and tools necessary for engineering applications and ability to use information technologies effectively.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO8** | **Recognition of the effects of engineering applications on health, environment and safety in the universal and societal dimensions and the problems of the time and awareness of the legal consequences of engineering solutions.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO9** | **Ability to identify, define, formulate and solve complex engineering problems; and electing and applying appropriate analysis and modelling methods for this purpose.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **Discipline Specific Outcomes (program)** | **PO10** | **Gains comprehensive knowledge in mathematics, natural sciences, related engineering fields and general engineering subjects.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO11** | **Able to identify complex engineering problems and solve them with appropriate methods of analysis.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO12** | **Able to design a complex electronic system that meets the desired performance by using modern design techniques and taking real life conditions into account.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO13** | **Able to develop new techniques and tools for solution of current engineering problems.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO14** | **Able to use computer software and hardware technologies together with information technologies in an effective way.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO15** | **Able to produce innovative solutions for solution of current engineering problems by gathering data through experiment design and interpretation of results.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO16** | **Able to actively work individually or in teams where engineers from the same or different disciplines are involved.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO17** | **Gains competency in effective written and verbal communication, presentation and preparation of technical reports in Turkish and English.**  | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO18** | **Constantly increases knowledge with the awareness of lifelong learning by closely following the developments in science and technology .** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO19** | **Acts in accordance with scientific and ethical principles and the standards used in engineering practice at every stage of career** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO20** | **Able to describe concepts related to business life such as project management, risk management, change management, entrepreneurship and sustainability.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO21** | **Gains awareness of the legal consequences of engineering solutions developed together with the effect of engineering applications on health, environment and safety on a universal and social scale.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO22** | **Able to solve problems involving probability and statistics, derivative and integral calculations, multivariable mathematics, linear algebra, differential equations, and complex variables, and their electrical and electronics applications.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO23** | **Able to organize projects and events for the social environment they live in with the awareness of social responsibility and implement them.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
| **PO24** | **Able to plan and direct activities for employees under their responsibility to develop within the framework of a project.** | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 | 🗸 |
|  |  |  |  |  |  |  |  |  |
| **PART III ( Department Board Approval)** |
| **Course Subjects, Contribution of Course Subjects to Learning Outcomes, and Methods for Assessing Learning of Course Subjects** | **Subjects** | **Week** |  | **LO1** | **LO2** | **LO3** | **LO4** | **LO5** | **LO6** |
| **S1** | *1* | **Introduction to Mathematics for Electrical and Electronic Engineering** (Definitions of signals, systems, measurement, error, time-frequencyconcept, sampling and coding basics, modulation concept) [Deniz Gençağa] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S2** | *2* | **Data analysis:** What is data acquisition, analog-to-digital conversion, different data formats, concepts of missing values, interpolation, extrapolation, examples on the physical interpretations of numbers in a data set [Deniz Gençağa] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S3** | *3-4* | **Introduction to programming for Electrical and Electronic Engineering** (The logic behind coding, conceptual introduction to low level and high level languages, data processing fundamentals) [Deniz Gençağa] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S4** | 5 | **An introduction to wireless communication systems and standard**s including 2G, 3G, 4G, 5G, and future 6G. The differences among them in terms of the provided services, technical and commercial requirements (data rate, reliability, latency, etc.), enabling technologies, and adopted multiplexing/accessing techniques (FDMA, TDAM, CDMA, OFDMA, NOMA). [Jehad Hamamreh]. | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S5** | 6 | **An introduction to wireless physical layer security**. The differences between physical layer security and upper layers security (i.e., classical symmetric and asymmetric cryptography), the key concepts, approaches, merits, and challenges of each direction. [Jehad Hamamreh]. | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S6** | 7-8 | **An Introduction to the open system interconnection (OSI) Model.** The 7 layers in computer networks and their functionalities (Layer 7: The application layer. Layer 6: The presentation layer. Layer 5: The session layer. Layer 4: The transport layer. Layer 3: The network layer. Layer 2: The data-link/MAC layer. Layer 1: The physical layer). [Jehad Hamamreh]. | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S7** | 9 | **Introduction to Electromagnetics:** Definition and examples for waves, electrostatics and magnetostatics [Yusuf ÖZTÜRK]. | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S8** | 10 | **Applications of Electromagnetics:** RF and Microwave Engineering Concepts and examples [Yusuf ÖZTÜRK]. | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S9** | 11 | **Introduction to the device growth process**, GaN-based materials growth for optoelectronic devices applications with MOCVD and MBE technique [Engin ARSLAN] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S10** | 12 | **Introduction to the device fabrication**, LED, HEMT and other devices fabrication techniques [Engin ARSLAN] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S11** | 13 | Basic Circuit Components and Circuit Theories [M. İlker Beyaz] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S12** | 14 | Analog and Digital Electronic Circuits [M. İlker Beyaz] | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* | *A1-A4-A8* |
| **S13** |  |  |  |  |  |  |  |  |
| **S14** |  |  |  |  |  |  |  |  |
| **Assessment Methods, Weight in Course Grade, Implementation and Make-Up Rules**  | **No.** | **Type** | **Weight** | **Implementation Rule** | **Make-Up Rule** |
| **A1** | **Exam** | *%100* | *No electronics allowed in the exams except calculators*  | The student is informed about a make-up exam in case his/her excuse is valid and an accompanying doctors report is provided. |
| **A2** | **Quiz** |  |  |  |
| **A3** | **Homework** |  |  |  |
| **A4** | **Project** |  |  |  |
| **A5** | **Report** |  | - | - |
| **A6** | **Presentation** |  | - | - |
| **A7** | **Attendance/ Interaction** |  |  |  |
| **A8** | **Class/Lab./****Field Work** |  |  |  |
| **A9** | **Other** |  |  |  |
| **TOTAL** | **100%** |
| **Evidence of Achievement of Learning Outcomes** | At least one question from each subject is asked during the examsA weighted average is calculated for each student based on the percentage of each assessment method. Students are required to collect a minimum score over 100, which is announced by the instructor, to pass the course. This score is determined based on class average. |
| **Method for Determining Letter Grade** | The scores of 2 test exams (Midterm and Final) are used to calculate the final score. Maximum scores from each contributor is shown below.

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Assesment | Midterm | Final |  |  |  | Total |
| Point | 50 | 50 |  |  |  | 100 |

The table below is used to conver the total point over 100 to letter grade:

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Point | 100-75 | 74-70  | 69-65 | 64-60 | 59-55 | 54-50 | 49-45 | 44-40 | 39-35 | 34-30 |
| Grade | A | A- | B+ | B | B- | C+ | C | C- | D+ | D |

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| **Teaching Methods, Student Work Load** | **No** | **Method** | **Explanation** | **Hours** |
| ***Time applied by instructor*** |
| **1** | **Lecture** | Class content is explained by writings on the board and computer presentations | *3x14* |
| **2** | **Interactive Lecture** |  |  |
| **3** | **Recitation** |  |  |
| **4** | **Laboratory** |  |  |
| **5** | **Practical** |  |  |
| **6** | **Field Work** |  |  |
| ***Time expected to be allocated by student*** |
| **7** | **Project** |  |  |
| **8** | **Homework** |  |  |
| **9** | **Pre-class Learning of Course Material**  |  |  |
| **10** | **Review of Course Material** | Previous class material is reviewed each week | *1x14* |
| **11** | **Studio** |  |  |
| **12** | **Office Hour** | One-to-one meetings for discussions | *1x14* |
| **TOTAL** | *70* |
| **IV. PART** |
| **Instructor** | **Name** | Yusuf Öztürk |
| **E-mail** | yusuf.ozturk@antalya.edu.tr |
| **Phone Number** | *0242 245 0312* |
| **Office Number** | *A1-27* |
| **Office Hours** | *Determined during each semester, 2 hours per week* |
| **Course Materials** | **Mandatory** |  |
| **Recommended** | Hambley, A. R. Electrical Engineering: Principles & Applications, Global Edition eBook (7e). Pearson Australia. This book is also a prescribed text for the subject 48520 Electronics and Circuits. It is available from the following link in Digital and/or Print. |
| **Other** | **Scholastic Honesty** | Violations of scholastic honesty include, but are not limited to cheating, plagiarizing, fabricating information or citations, facilitating acts of dishonesty by others, having unauthorized possession of examinations, submitting work of another person or work previously used without informing the instructor, or tampering with the academic work of other students. Any for of scholastic dishonesty is a serious academic violation and will result in a disciplinary action. |
| **Students with disabilities** | Reasonable accommodations will be made for students with verifiable disabilities. |
| **Safety Issues**  | The course does not require any special safety precautions. |
| **Flexibility** | Circumstances may arise during the course that prevents the instructor from fulfilling each and every component of this syllabus; therefore, the syllabus is subject to change.  Students will be notified prior to any changes.  |